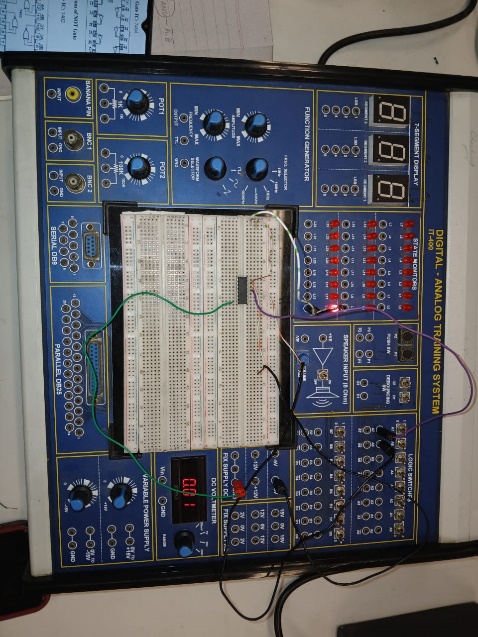
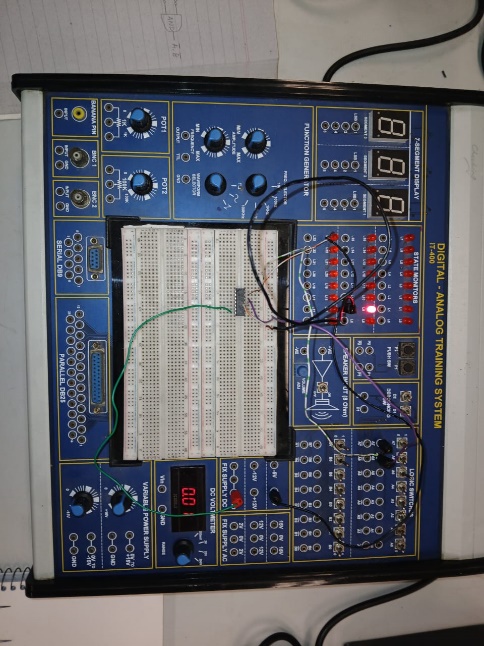
**LAB 5 PHOTOGRAPHS**

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NAND Gate

OR Gate with Inverted inputs

**DE MORGAN’S 2nd THEORM:**

NOR Gate

AND Gate with Inverted inputs

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**DE MORGAN’S 1st THEORM:**